

XCF08PVOG48C Xilinx

XCF08PVOG48C Xilinx is an E EPROM memory chip introduced by Xilinx, one of the Platform Flash series. With the ease of use, high efficiency, and reprogramming, programmable flash devices within the system are used to configure Xilinx FPGAs. For more high-quality electronic components, please contact XT-ShenZhen@!!!

As a professional XT-ShenZhen® XCF08PVOG48C Xilinx supplier, we would like to provide you XCF08PVOG48C Xilinx. XCF08PVOG48C Xilinx is an E EPROM memory chip introduced by Xilinx, one of the Platform Flash series. With the ease of use, high efficiency, and reprogramming, programmable flash devices within the system are used to configure Xilinx FPGAs. For more high-quality electronic components, please contact XT-ShenZhen@!!!

Product description

The XCF08PVOG48C Xilinx introduces an internal programmable configuration PROM. Offering densities from 1Mb to 32 Mb, these PROMs provide an easy-to-use, cost-effective, and reprogrammable way to store large Xilinx FPGA configuration bitstreams. The Platform Flash PROM family includes 3.3V XCFxxS PROM and 1.8V XCFxxP PROM. The XCFxxS version includes 4 Mb, 2 Mb, and 1 Mb PROM with support for master and slave serials. The XCFxxP version includes 32 Mb, 16 Mb, and 8 Mb PROM supporting Master Serial, Slave Serial, Master SelectMAP, and Slave SelectMAP FPGA configuration modes. When driven by a stable external clock, the PROM can output data at rates up to 33 MHz.

Product features

- On-system programmable PROM for configuring Xilinx® FPGAs
- Low power consumption advanced CMOS NOR flash process
- Durability of 20,000 programming/erase cycles
- JTAG commands for standard FPGA configuration are initiated
- Longer or multiple bitstreams can be cascaded
- The I/O pins are compatible with voltage levels from 1.8V to 3.3V
- Design support using Xilinx ISE® Alliance and Foundation™ packages
- Serial or parallel FPGA configuration interface
- Design revision technology supports storing and accessing multiple design revisions configured
- Built-in data decompressor compatible with Xilinx advanced compression technology

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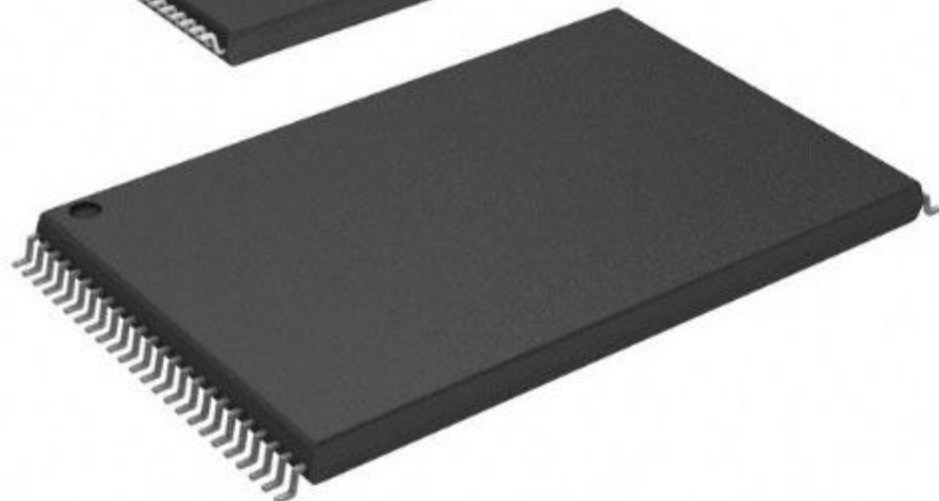
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Product parameters

Technical parameters	Supply voltage (DC).	1.80 V, 2.00 V (max)
	The number of pins	48
	Clock frequency	50MHz
	Operating Temperature (Max)	85°C
	Operating Temperature(Min)	-40°C
	Supply voltage (Max)	2V
	Supply voltage (Min)	1.65V
	encapsulation	TSOP-48
	Packaging	Tube
	Leaded standard	Lead Free
	RoHS standard	RoHS Compliant

Product images



FAQ

Q: Are you a trader or a manufacturer?

A: Yes, we are traders. The XCF08PVOG48C Xilinx we sell are purchased directly from the factory.

Q: What is your MOQ?

A: We will decide according to the product packaging method, and will communicate with you in time before placing an order.

Q: Are your shipments fast?

A: Yes, we have our own warehouse, and most of the goods are in stock. Same-day delivery can be achieved as soon as possible.